

PROGRAM: M.Tech.-Communication Systems (5 Yrs Integrated)

SEMESTER: 4

TIMETABLE-Section A

	I	II	III	IV	V	VI	VII	VIII	IX	X	XI	XII
	08:40-09:30	09:30-10:20	10:20-11:10	11:30-12:20	12:20-13:10	13:10-14:00	14:20-15:10	15:10-16:00	16:00-16:50	17:00-17:50	17:50-18:40	18:40-19:30
Mon	MCSCEC405-A	MCSCEC405-A	MCSCEC405-A	MCSDEC402-A		MCSCEC404-A	MCSCEC403-A	MCSMA401-A	MCSDEC401-A			
	MCSCEC406-A	MCSCEC406-A	MCSCEC406-A									
Tue		MCSDEC402-A	MCSMA401-A	MCSCEC404-A	MCSCEC402-A		MCSCEC405-A	MCSCEC405-A	MCSCEC405-A			
							MCSCEC406-A	MCSCEC406-A	MCSCEC406-A			
Wed	MCSCEC402-A	MCSCEC404-A		MCSMA401-A		MCSCEC403-A	MCSDEC402-A	MCSDEC401-A				
	MCSMA401-A	MCSDEC401-A	MCSCEC402-A	MCSCEC405-A	MCSCEC405-A	MCSCEC405-A		MCSCEC403-A				
Thu				MCSCEC406-A	MCSCEC406-A	MCSCEC406-A						
Fri	MCSCEC403-A	MCSCEC402-A	MCSCEC404-A	MCSDEC401-A		MCSDEC402-A	MCSMA401-A	MCSCTP407-A				
Sat												

Code	Description	Section	Faculty	Venue
MCSCEC402	NETWORK THEORY	A	Padma Priya, V	VV316, VIDHYUTH VIHAR
MCSCEC403	LINEAR INTEGRATED CIRCUITS	A	Susan, D	VV316, VIDHYUTH VIHAR
MCSCEC404	SIGNALS AND SYSTEMS	A	John Bosco Bahaguru, R	VV316, VIDHYUTH VIHAR
MCSCEC405	CIRCUITS AND SYSTEM SIMULATION LAB	A	Avila, J	LAB, VIDHYUTH VIHAR
MCSCEC406	LINEAR INTEGRATED CIRCUITS LAB	A	Susan, D	LAB, VIDHYUTH VIHAR
MCSMA401	DISCRETE MATHEMATICS	A	Balaji, S	VV316, VIDHYUTH VIHAR
MCSCTP407	HR SKILLS-II	A	Chandrasekar, V	GNV205, GNANA VIHAR
MCSDEC401	PULSE AND WAVE SHAPING CIRCUITS	A	Avila, J	VV316, VIDHYUTH VIHAR
MCSDEC402	CONTROL ENGINEERING	A	Venkatesh, S	VV316, VIDHYUTH VIHAR

TIMETABLE-Section B

	I	II	III	IV	V	VI	VII	VIII	IX	X	XI	XII
	08:40-09:30	09:30-10:20	10:20-11:10	11:30-12:20	12:20-13:10	13:10-14:00	14:20-15:10	15:10-16:00	16:00-16:50	17:00-17:50	17:50-18:40	18:40-19:30
Mon		MCSMA401-B	MCSCEC403-B	MCSCEC405-B	MCSCEC405-B	MCSCEC405-B		MCSDEC402-B	MCSCEC404-B			
				MCSCEC406-B	MCSCEC406-B	MCSCEC406-B						
Tue		MCSCEC402-B	MCSCEC403-B	MCSDEC402-B		MCSMA401-B	MCSCEC404-B	MCSDEC401-B				
	MCSCEC405-B	MCSCEC405-B	MCSCEC405-B	MCSCEC402-B	MCSCEC403-B	MCSDEC402-B		MCSMA401-B				
Wed												
	MCSCEC406-B	MCSCEC406-B	MCSCEC406-B									
Thu	MCSDEC401-B	MCSDEC402-B	MCSCEC404-B	MCSMA401-B	MCSCEC402-B		MCSCEC405-B	MCSCEC405-B	MCSCEC405-B			
							MCSCEC406-B	MCSCEC406-B	MCSCEC406-B			
Fri	MCSCTP407-B	MCSDEC401-B	MCSMA401-B	MCSCEC402-B		MCSCEC403-B	MCSDEC401-B	MCSCEC404-B				
Sat												

Code	Description	Section	Faculty	Venue
MCSCEC402	NETWORK THEORY	B	Viswanathan, B	VV205, VIDHYUTH VIHAR
MCSCEC403	LINEAR INTEGRATED CIRCUITS	B	Wilson, K J	VV205, VIDHYUTH VIHAR
MCSCEC404	SIGNALS AND SYSTEMS	B	Balu, R	VV205, VIDHYUTH VIHAR
MCSCEC405	CIRCUITS AND SYSTEM SIMULATION LAB	B	Lakshmi, C	LAB, VIDHYUTH VIHAR
MCSCEC406	LINEAR INTEGRATED CIRCUITS LAB	B	Prasath, R K	LAB, VIDHYUTH VIHAR
MCSMA401	DISCRETE MATHEMATICS	B	Natarajan, C	VV205, VIDHYUTH VIHAR
MCSCTP407	HR SKILLS-II	B	Brinda Kailash Giri	GNV311, GNANA VIHAR
MCSDEC401	PULSE AND WAVE SHAPING CIRCUITS	B	Jeyaprakash, B G	VV205, VIDHYUTH VIHAR
MCSDEC402	CONTROL ENGINEERING	B	Manigandan, N S	VV205, VIDHYUTH VIHAR

TIMETABLE-Section C

	I	II	III	IV	V	VI	VII	VIII	IX	X	XI	XII
	08:40-09:30	09:30-10:20	10:20-11:10	11:30-12:20	12:20-13:10	13:10-14:00	14:20-15:10	15:10-16:00	16:00-16:50	17:00-17:50	17:50-18:40	18:40-19:30
Mon		MCSCEC404-C	MCSDEC401-C	MCSMA401-C	MCSDEC402-C		MCSCEC405-C	MCSCEC405-C	MCSCEC405-C			
							MCSCEC406-C	MCSCEC406-C	MCSCEC406-C			
Tue		MCSCEC402-C	MCSDEC402-C	MCSCEC404-C		MCSCEC403-C	MCSMA401-C	MCSDEC401-C				
	MCSMA401-C	MCSCEC402-C	MCSCEC403-C	MCSCEC405-C	MCSCEC405-C	MCSCEC405-C		MCSDEC402-C				
Wed												
				MCSCEC406-C	MCSCEC406-C	MCSCEC406-C						
Thu	MCSDEC402-C	MCSCEC403-C	MCSCEC402-C	MCSDEC401-C		MCSMA401-C	MCSCEC404-C	MCSDEC401-C	MCSCTP407-C			
	MCSCEC405-C	MCSCEC405-C	MCSCEC405-C	MCSCEC403-C	MCSMA401-C		MCSCEC402-C	MCSCEC404-C				
Fri												
	MCSCEC406-C	MCSCEC406-C	MCSCEC406-C									

Sat

Code	Description	Section	Faculty	Venue
MCSCEC402	NETWORK THEORY	C	Susan, D	VV206, VIDHYUTH VIHAR
MCSCEC403	LINEAR INTEGRATED CIRCUITS	C	Wilson, K J	VV206, VIDHYUTH VIHAR
MCSCEC404	SIGNALS AND SYSTEMS	C	Chandrasekar, N	VV206, VIDHYUTH VIHAR
MCSCEC405	CIRCUITS AND SYSTEM SIMULATION LAB	C	Jaiseeli, C	LAB, VIDHYUTH VIHAR
MCSCEC406	LINEAR INTEGRATED CIRCUITS LAB	C	Asokan, J	LAB, VIDHYUTH VIHAR
MCSMA401	DISCRETE MATHEMATICS	C	Bahji, S	VV206, VIDHYUTH VIHAR
MCSCTP407	HR SKILLS-II	C	Ravi, R	GNV313, GNANA VIHAR
MCSDEC401	PULSE AND WAVE SHAPING CIRCUITS	C	Asokan, J	VV206, VIDHYUTH VIHAR
MCSDEC402	CONTROL ENGINEERING	C	Venkatesh, S	VV206, VIDHYUTH VIHAR

TIMETABLE-Section D

	I	II	III	IV	V	VI	VII	VIII	IX	X	XI	XII
	08:40-09:30	09:30-10:20	10:20-11:10	11:30-12:20	12:20-13:10	13:10-14:00	14:20-15:10	15:10-16:00	16:00-16:50	17:00-17:50	17:50-18:40	18:40-19:30
Mon	MCSDEC402-D	MCSCEC402-D	MCSDEC401-D	MCSCEC404-D		MCSCEC403-D	MCSCMA401-D	MCSCTP407-D	MCSCEC404-D			
Tue	MCSCEC405-D	MCSCEC405-D	MCSCEC405-D	MCSCMA401-D		MCSDEC401-D	MCSCEC402-D	MCSCEC403-D				
Wed		MCSCEC406-D	MCSCEC406-D	MCSCEC406-D								
Thu	MCSCMA401-D	MCSCEC402-D	MCSDEC402-D	MCSDEC401-D		MCSCEC403-D		MCSCEC405-D	MCSCEC405-D	MCSCEC405-D		
Fri		MCSCEC402-D	MCSCEC403-D	MCSCEC405-D	MCSCEC405-D	MCSCEC405-D		MCSDEC402-D	MCSCMA401-D			
Sat				MCSCEC406-D	MCSCEC406-D	MCSCEC406-D						

Code	Description	Section	Faculty	Venue
MCSCEC402	NETWORK THEORY	D	Lakshmi, C	VV213, VIDHYUTH VIHAR
MCSCEC403	LINEAR INTEGRATED CIRCUITS	D	Rakesh Kumar Karn	VV213, VIDHYUTH VIHAR
MCSCEC404	SIGNALS AND SYSTEMS	D	Raj Kumar, G	VV213, VIDHYUTH VIHAR
MCSCEC405	CIRCUITS AND SYSTEM SIMULATION LAB	D	Raj Kumar, G	LAB, VIDHYUTH VIHAR
MCSCEC406	LINEAR INTEGRATED CIRCUITS LAB	D	Balu, R	LAB, VIDHYUTH VIHAR
MCSCMA401	DISCRETE MATHEMATICS	D	Rajaraman, R	VV213, VIDHYUTH VIHAR
MCSCTP407	HR SKILLS-II	D	Chandrasekar, V	GNV205, GNANA VIHAR
MCSDEC401	PULSE AND WAVE SHAPING CIRCUITS	D	Chandrasekar, M	VV213, VIDHYUTH VIHAR
MCSDEC402	CONTROL ENGINEERING	D	Gomathi, V	VV213, VIDHYUTH VIHAR

TIMETABLE-Section E

	I	II	III	IV	V	VI	VII	VIII	IX	X	XI	XII
	08:40-09:30	09:30-10:20	10:20-11:10	11:30-12:20	12:20-13:10	13:10-14:00	14:20-15:10	15:10-16:00	16:00-16:50	17:00-17:50	17:50-18:40	18:40-19:30
Mon	MCSCEC403-E	MCSCEC402-E	MCSDEC401-E	MCSCTP407-E		MCSCEC404-E	MCSDEC402-E	MCSCMA401-E				
Tue	MCSCEC404-E	MCSDEC402-E	MCSCMA401-E	MCSCEC405-E	MCSCEC405-E	MCSCEC405-E		MCSCEC402-E				
Wed			MCSCEC406-E	MCSCEC406-E	MCSCEC406-E							
Thu		MCSDEC401-E	MCSCEC402-E	MCSCMA401-E		MCSCEC404-E	MCSDEC402-E	MCSCEC403-E	MCSDEC401-E			
Fri	MCSCEC405-E	MCSCEC405-E	MCSCEC405-E	MCSCEC403-E	MCSDEC402-E		MCSCMA401-E	MCSDEC401-E				
Sat	MCSCEC406-E	MCSCEC406-E	MCSCEC406-E									
Fri		MCSCEC403-E	MCSCEC402-E	MCSCEC404-E	MCSCMA401-E		MCSCEC405-E	MCSCEC405-E	MCSCEC405-E			
Sat							MCSCEC406-E	MCSCEC406-E	MCSCEC406-E			

Code	Description	Section	Faculty	Venue
MCSCEC402	NETWORK THEORY	E	Narayanan, M	VV214, VIDHYUTH VIHAR
MCSCEC403	LINEAR INTEGRATED CIRCUITS	E	Wilson, K J	VV214, VIDHYUTH VIHAR
MCSCEC404	SIGNALS AND SYSTEMS	E	Parvathy, A	VV214, VIDHYUTH VIHAR
MCSCEC405	CIRCUITS AND SYSTEM SIMULATION LAB	E	Ganapathy, R	LAB, VIDHYUTH VIHAR
MCSCEC406	LINEAR INTEGRATED CIRCUITS LAB	E	Raajan, N R	LAB, VIDHYUTH VIHAR
MCSCMA401	DISCRETE MATHEMATICS	E	Rajaraman, R	VV214, VIDHYUTH VIHAR
MCSCTP407	HR SKILLS-II	E	Ravi, R	GNV313, GNANA VIHAR
MCSDEC401	PULSE AND WAVE SHAPING CIRCUITS	E	Easwaran, M	VV214, VIDHYUTH VIHAR
MCSDEC402	CONTROL ENGINEERING	E	Manigandan, N S	VV214, VIDHYUTH VIHAR